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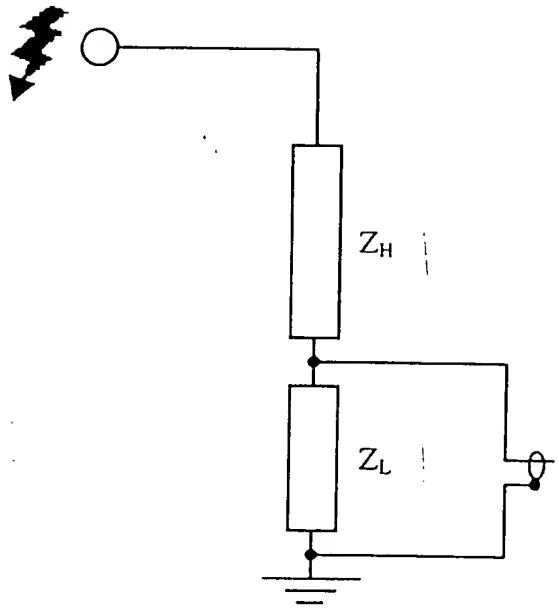

Unbuffered divider.

Figure 1

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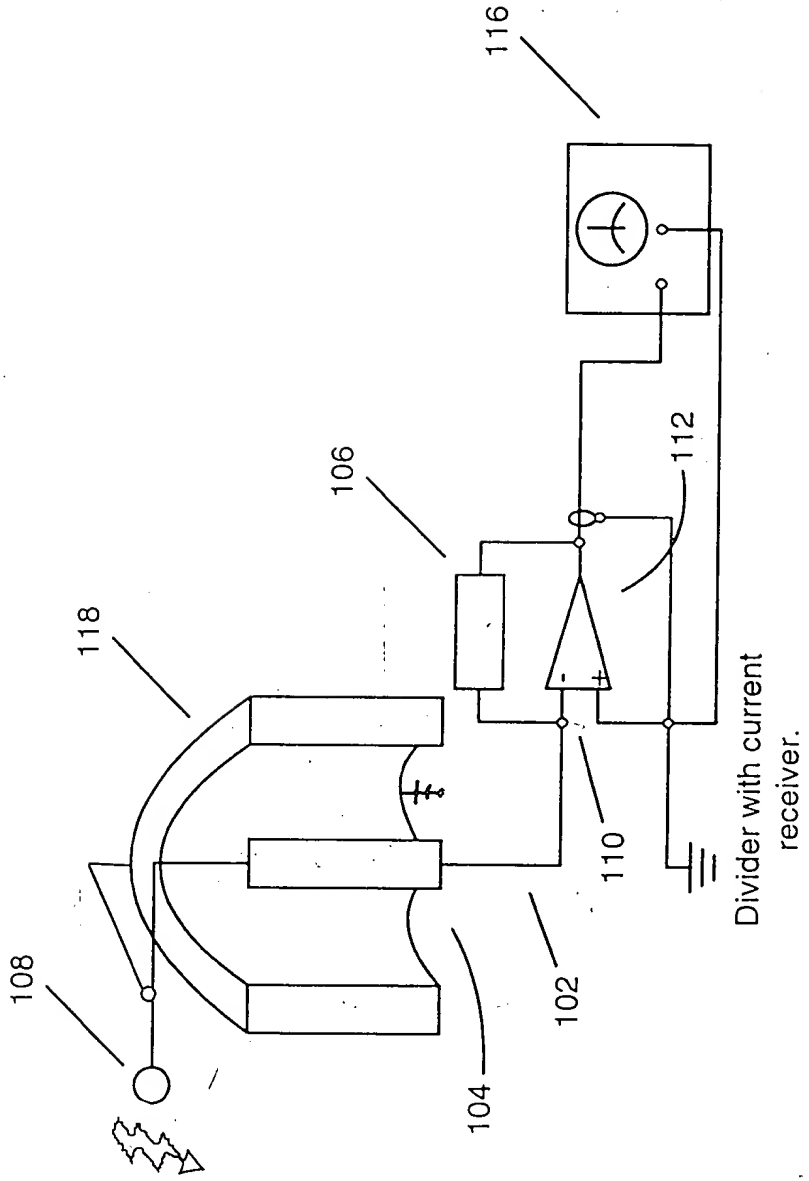

Figure 2

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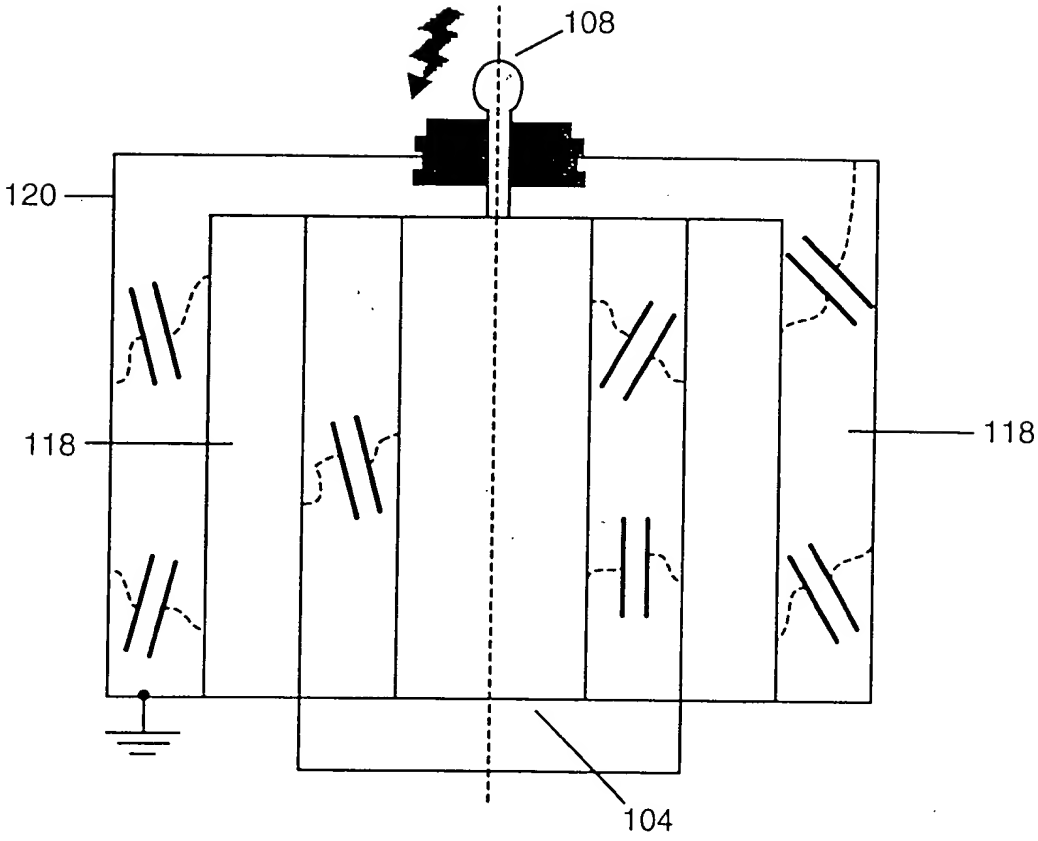

Figure 3

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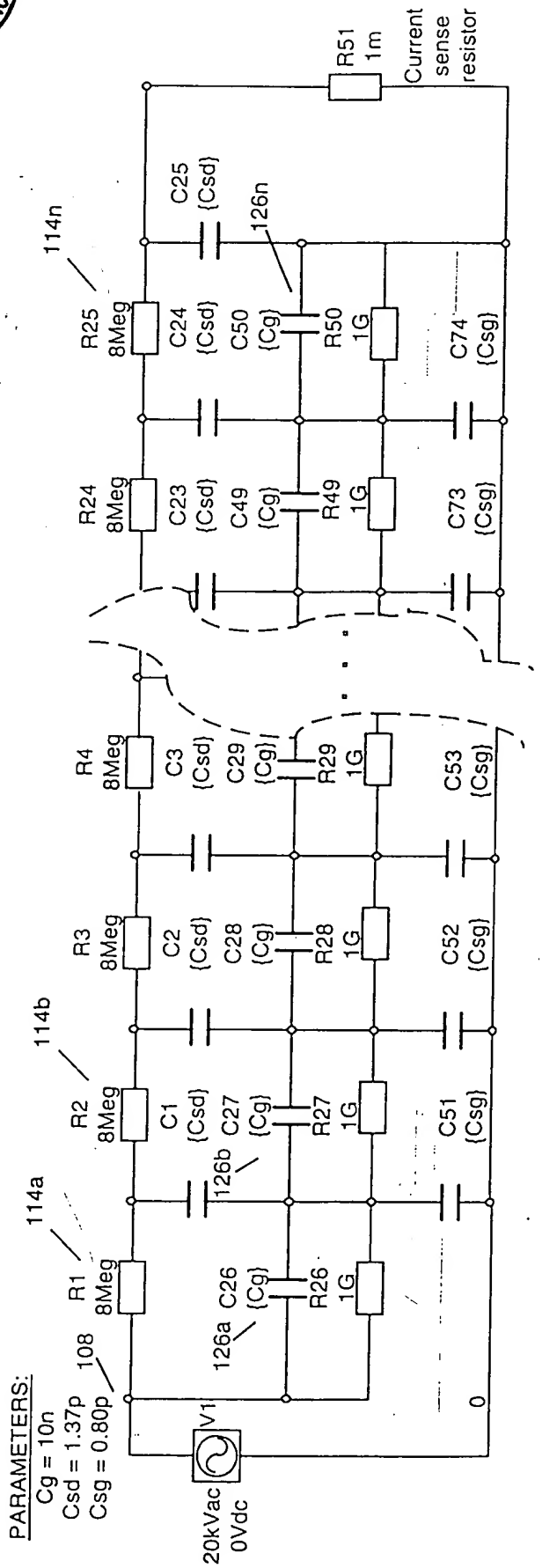

Figure 4

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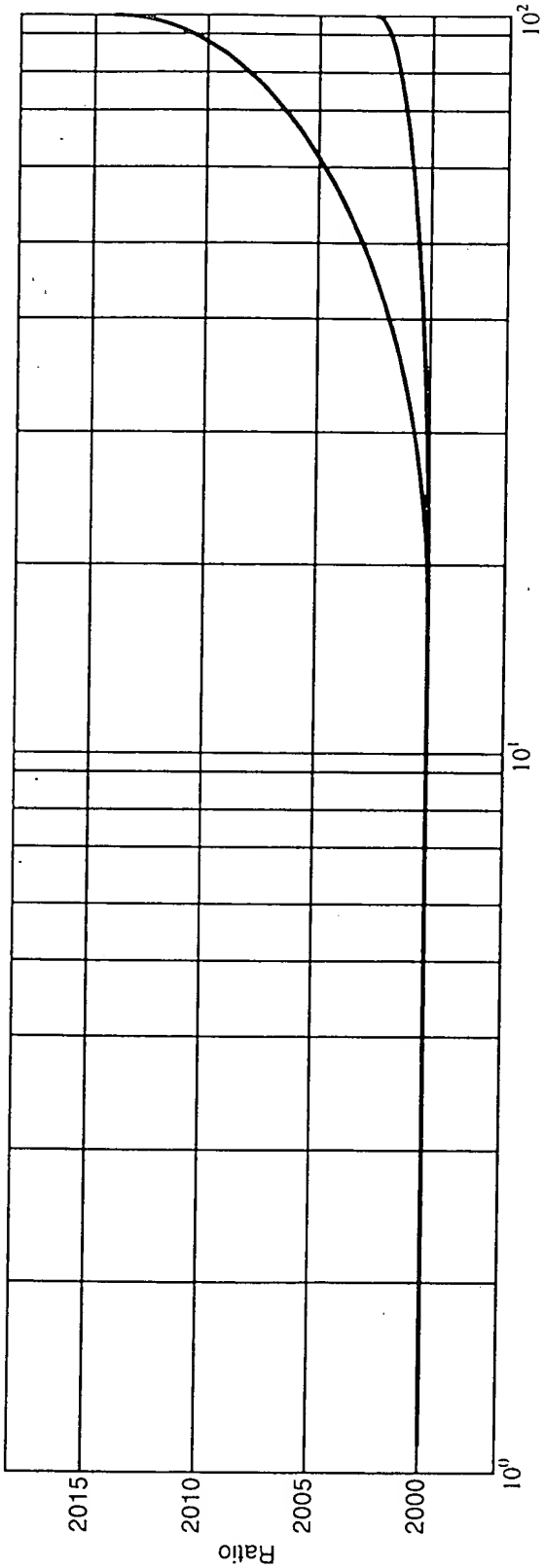

Figure 5

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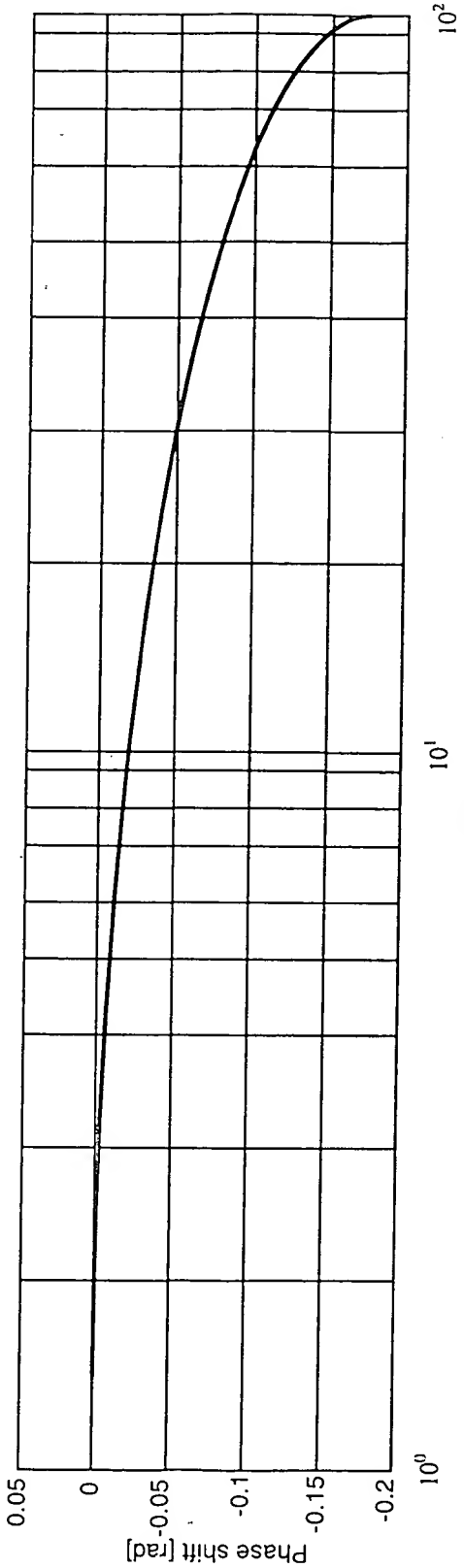

Figure 6

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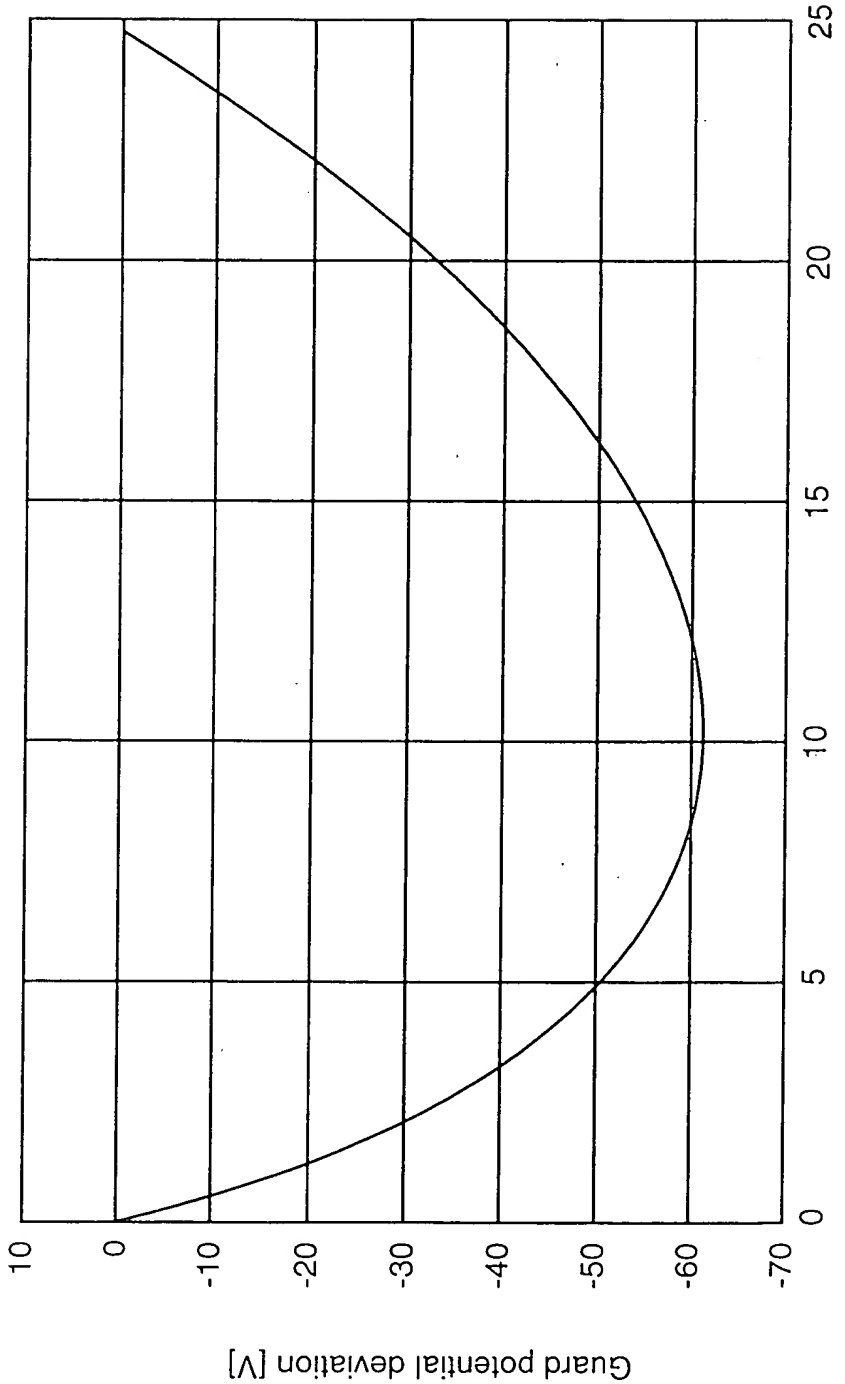

Figure 7

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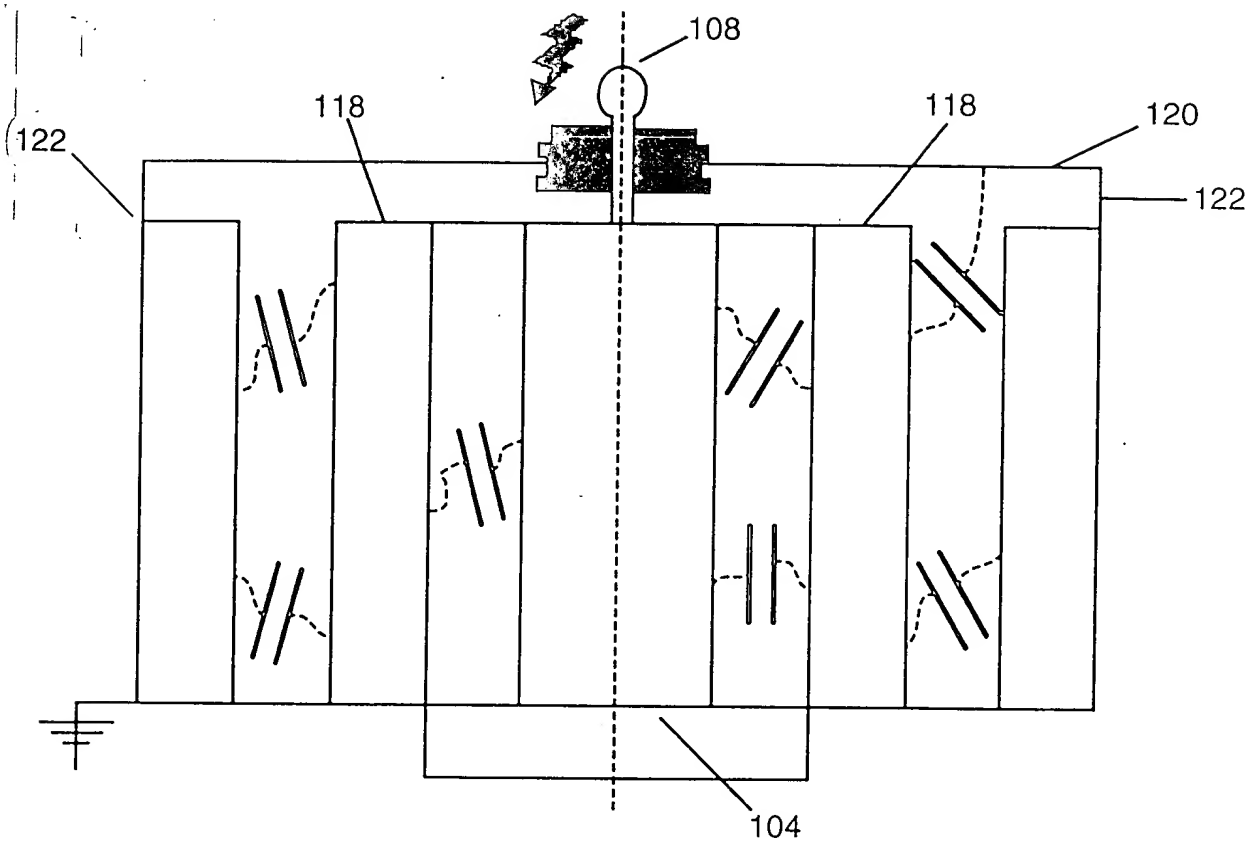

Figure 8

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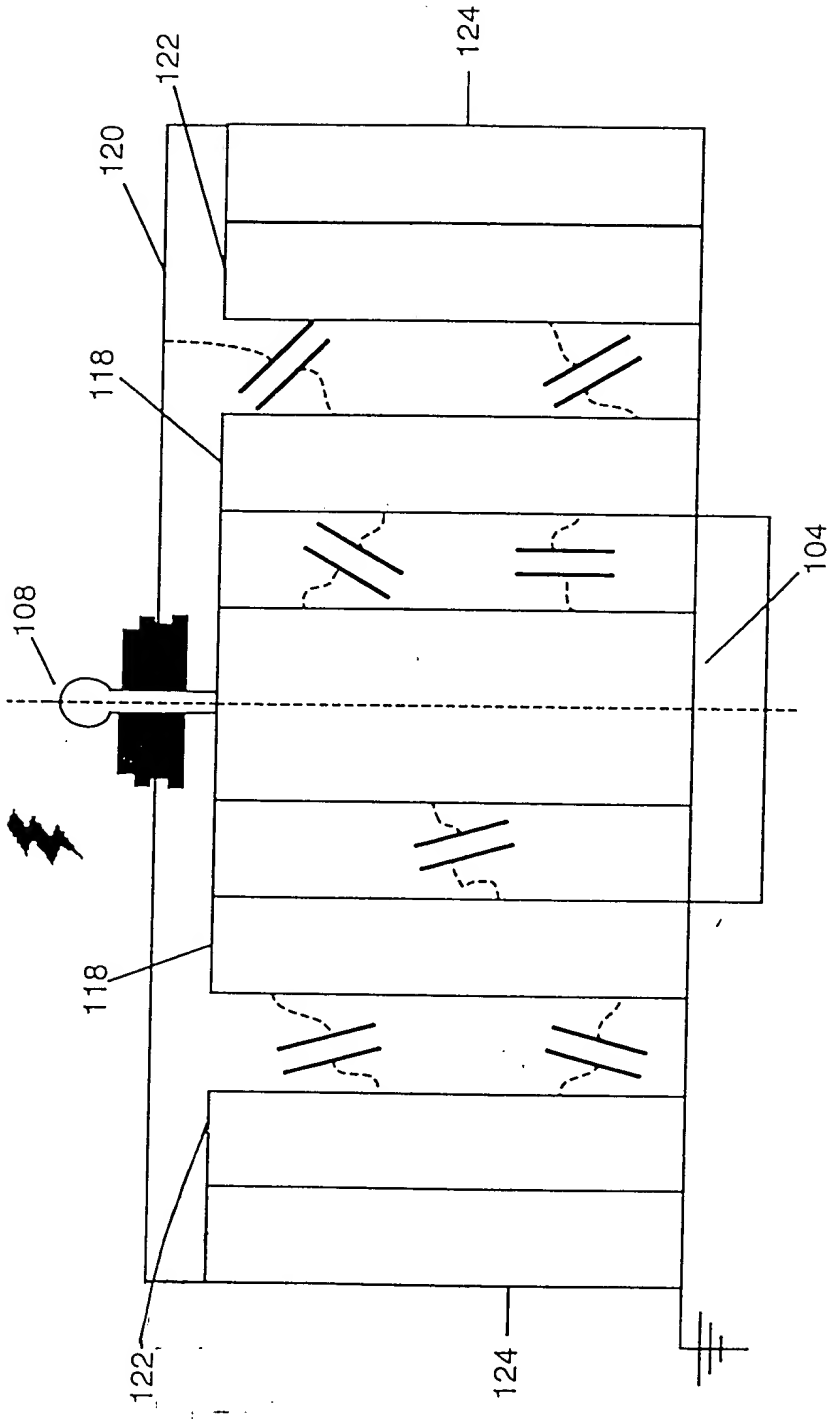

Figure 9

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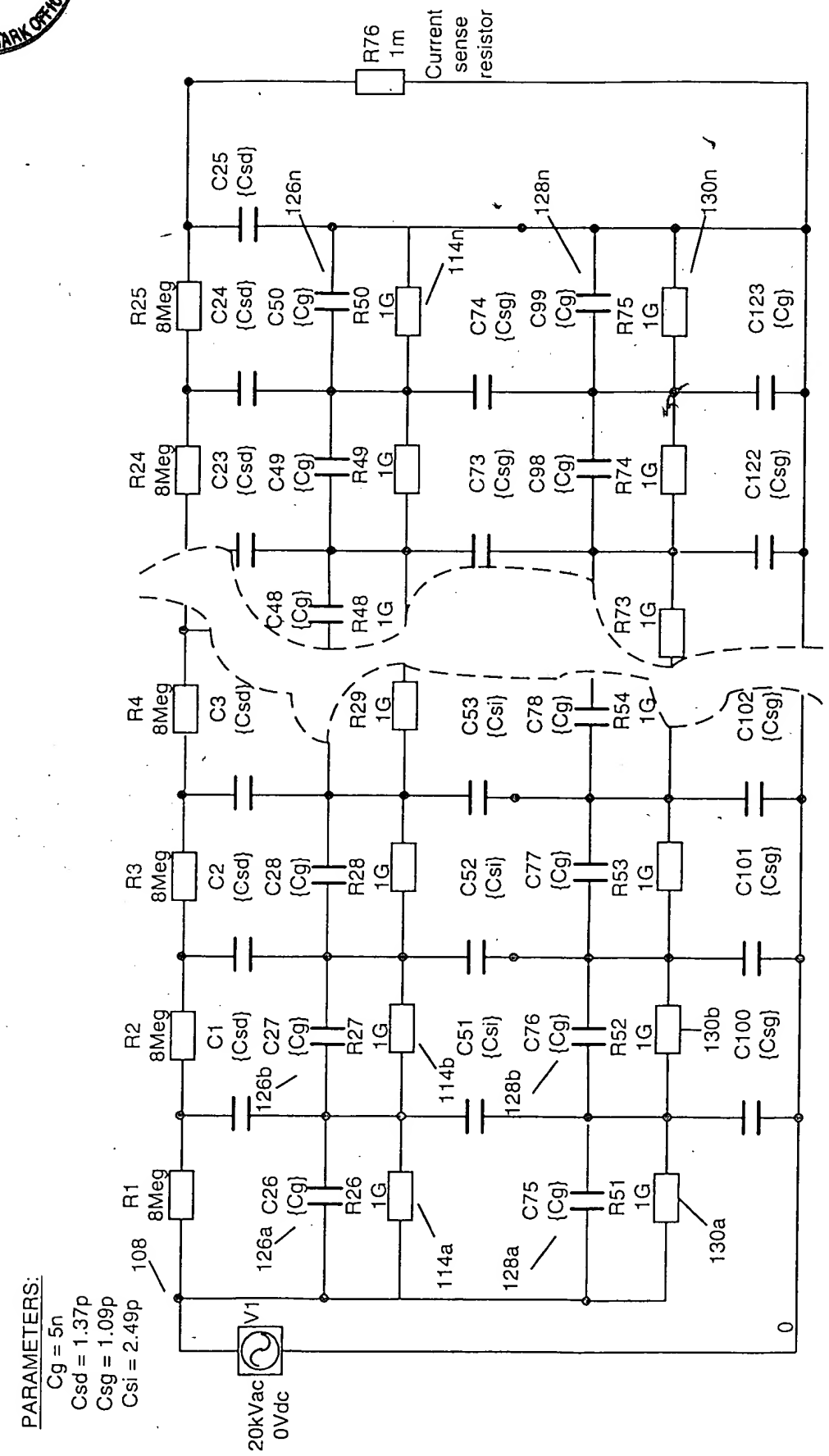

Figure 10

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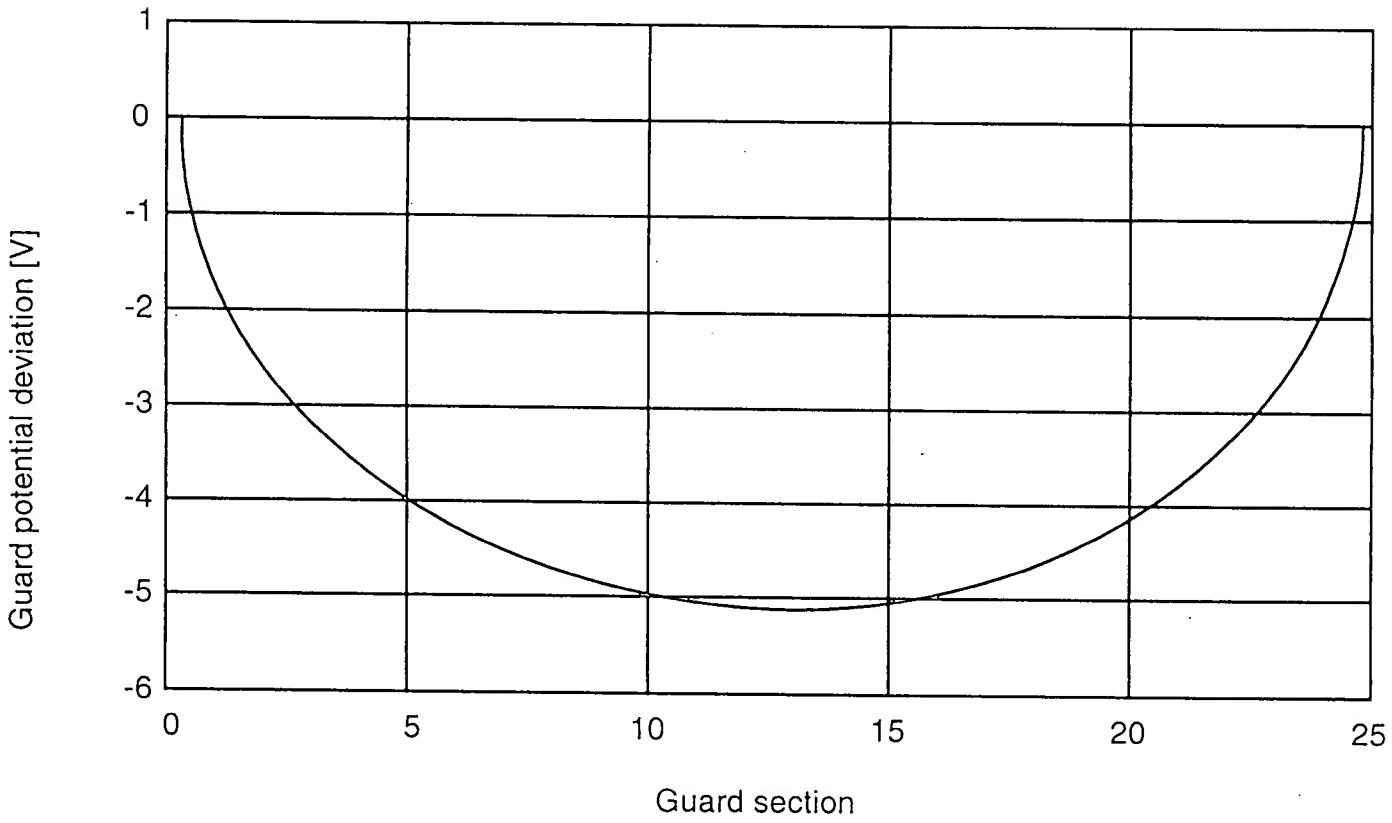

Deviation of the guard potential from ideal potential

Figure 11

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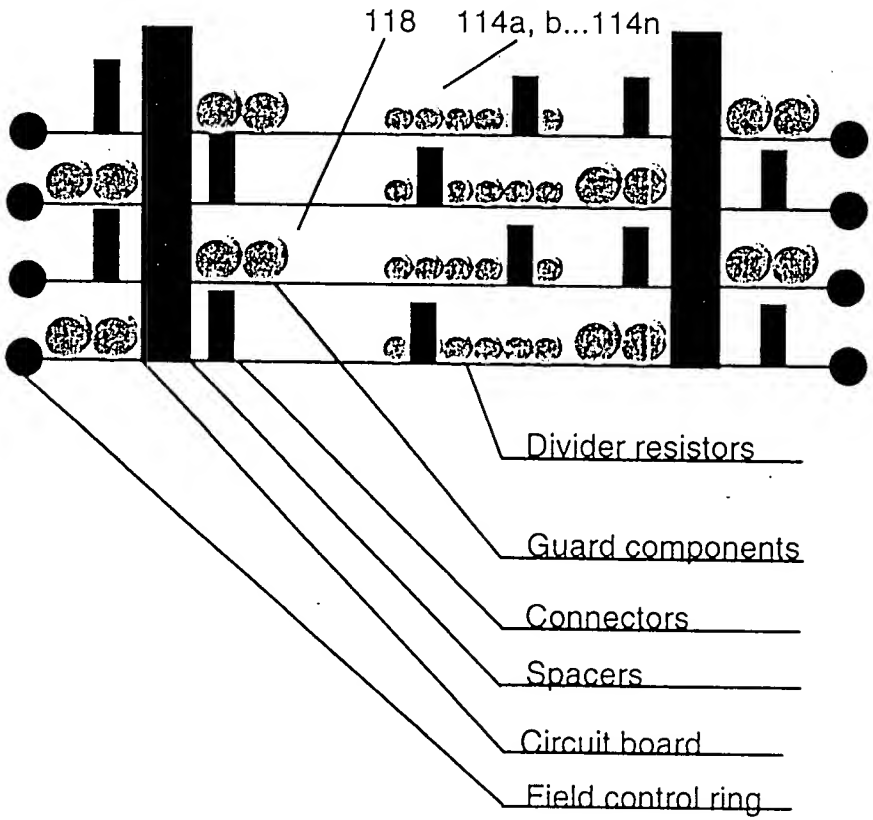

Cross-sectional view of four circuit boards in a divider stack

Figure 12

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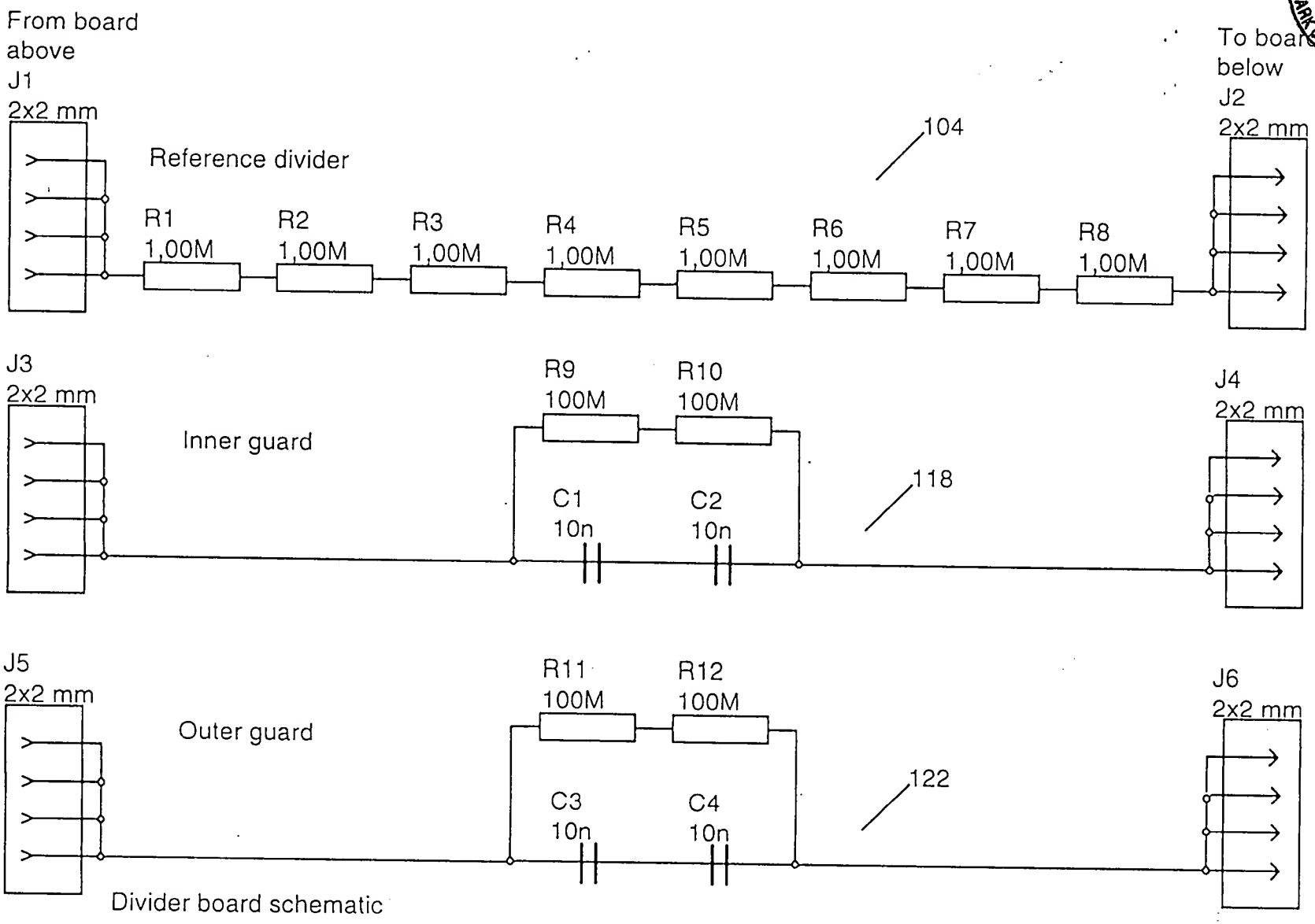

Figure 13

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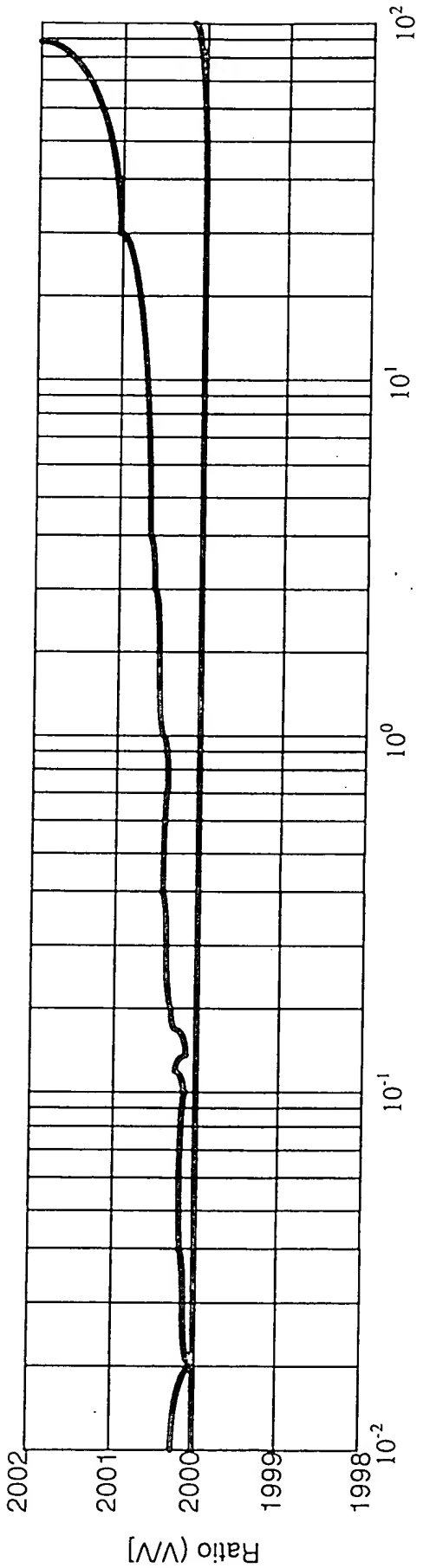

Figure 14

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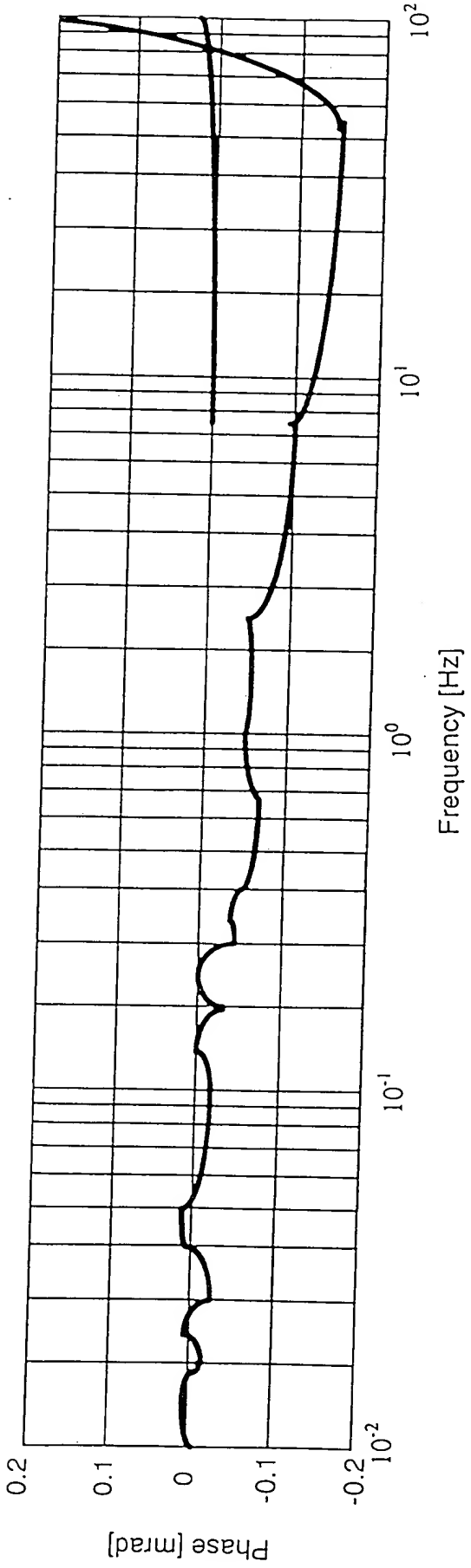

Figure 15

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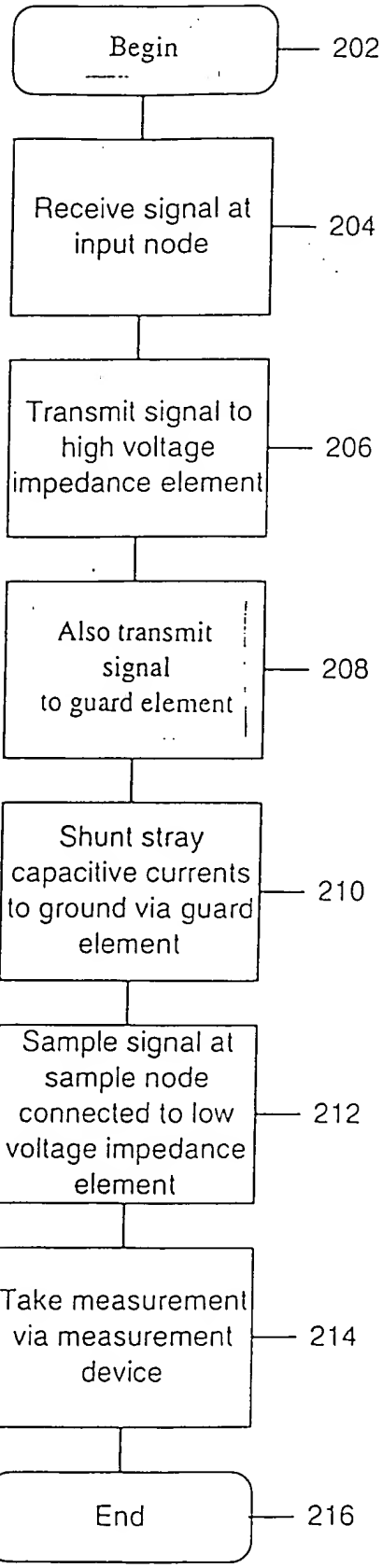

Figure 16